



## 2020 Functional Safety Working Group (FSWG)

Virtual Event#1  
November 17, 2020 - 10:00AM - 7:00PM EST

### Preliminary Agenda

Topic	Time
State of the Union	10:00 - 10:45AM
Zynq UltraScale+ ISO 13849 Certification	10:45 - 11:30AM
Break	11:30 - 11:45AM
ASIL B in Versal & Zynq UltraScale+ FPD	11:45 - 12:45PM
PL FMEDA - Leveraging PL Features & SEMIP	12:45 - 1:15PM
Functional Safety Solutions for Zynq UltraScale+	1:15 - 2:00PM
Lunch	2:00 - 3:00 PM
Versal Functional Safety Architecture	3:00 - 4:00PM
ASIL D in Versal & Zynq UltraScale+	4:00 - 4:30PM
Break	4:30 - 4:45PM
What to Know Before Starting a Design?	4:45 - 5:30PM
Security in Functional Safety Applications	5:30 - 6:15PM
Xilinx Functional Safety Roadmap	6:15 - 7:00PM

### Event Wrapup