

## Xilinx Executive Bios

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**Victor Peng**  
**President and Chief Executive Officer**

Victor Peng is president and chief executive officer of Xilinx, Inc., and serves on the board of directors. Peng has more than 30 years of experience defining and delivering leadership technologies across FPGAs, SoCs, GPUs, high-performance microprocessors and chip sets, and microprocessor IP products.

Since becoming CEO of Xilinx in February of 2018, Peng has rolled out plans for a transformation to address new markets with new technology, underpinned by the Adaptive Compute Acceleration Platform (ACAP). In his previous role as chief operating officer, Peng managed global sales, product and vertical marketing, product development, and global operations and quality. Prior to that, he served as executive vice president and general manager of products, where he led the definition, development, and product marketing of the company's portfolio of products and differentiated technologies – resulting in three consecutive generations of core product leadership and significant industry breakouts in integration and programming.

Peng previously served as corporate vice president of the graphics products group (GPG) silicon engineering with AMD and was a leader for AMD's central silicon engineering team supporting graphics, console game products, CPU chipset and consumer business units. Prior to that, Peng held executive and engineering leadership roles at TZero Technologies, MIPS Technologies, SGI and Digital Equipment Corp.

Peng holds four U.S. patents and serves on the boards of the Semiconductor Industry Association and KLA Corporation, a developer of industry-leading equipment and services for electronics industry. He earned a bachelor's degree in electrical engineering from Rensselaer Polytechnic Institute and a master's degree, also in electrical engineering, from Cornell University.



**Ivo Bolsens**  
**Senior Vice President and Chief Technology Officer**

Ivo Bolsens is senior vice president and chief technology officer, with responsibility for advanced technology development, Xilinx research laboratories and the company's university program. The research of his team led to the industry-leading adoption of 2.5D advanced packaging technology in Xilinx products. Bolsens' team also spearheaded the introduction of high-level abstraction flows for programming FPGA's, resulting in the first deployments in data center applications. Most recently, his team was credited for the development of Xilinx's AI Engine architecture, providing leading edge performance for machine learning applications in the 7nm Versal product family, the first generation of Xilinx's adaptive compute acceleration platform.

Bolsens came to Xilinx in June 2001 from the Belgium-based research center IMEC, where he was vice president of information and communication systems. His research included the development of verification for very-large-scale integration circuits, design of digital signal processing applications, and wireless communication terminals. He also headed the research on design technology for high-level synthesis of DSP hardware, hardware/software co-design and system-on-chip design.

Bolsens holds a PhD in applied science and an MSEE from the Catholic University of Leuven in Belgium.



**Liam Madden**  
**Executive Vice President and General Manager, Wired and Wireless Group**

Liam Madden is at the helm of the Wired and Wireless Group (WWG) at Xilinx, charged with driving the company's communications business and positioning Xilinx as the platform of choice in wired and wireless applications. Under his leadership, WWG is building upon Xilinx's momentum in SerDes technology and data converters and will drive critical soft IP. Madden is responsible for expanding Xilinx's communications business, leveraging the ongoing disaggregation in the communication ecosystem, and accelerating total time to market for Xilinx's customers.

Since joining Xilinx in 2008, Madden has been responsible for all Xilinx chip development, advanced package technology and system design. He has been instrumental to Xilinx achieving silicon product leadership with the highly successful 28nm, 20nm and 16nm product families. He is also responsible for the introduction of stacking technology at 28nm, leading the industry in integrating multiple devices on a silicon interposer for which he received the 2013 Semi Award. Madden is also credited with integrating Gb/s data converters into Xilinx products as part of the RFSoc product line, aimed at revolutionizing 5G radio solutions and related products. Most recently, Madden oversaw the release of Versal – Xilinx's first 7nm product offering and the industry's first Adaptive Compute Acceleration Platform (ACAP), as well as Alveo, a portfolio of powerful new accelerator cards targeting the data center.

Prior to joining Xilinx, Madden was a senior fellow at AMD, where he drove the next-generation chip integration methodology. Over the course of his distinguished career, Madden also contributed to a range of industry-leading products, including high performance and low power microprocessors (Alpha and StrongArm at DEC), embedded processors and IP (MIPS), and consumer devices (Xbox 360 at Microsoft).

Madden holds a Bachelor of Engineering degree from University College Dublin (UCD) and a Master of Engineering degree from Cornell University. He has served as an Adjunct Professor at UCD and is a Fellow of the Institute of Engineers Ireland. He also sits on the board of the Science Foundation Ireland.



**Salil Raje**  
**Executive Vice President and General Manager, Data Center Group**

Salil Raje heads the Data Center Group (DCG) for Xilinx, leading a global team of engineering, sales, and marketing professionals dedicated to the data center – the fastest growing market for FPGAs. Raje is excited about his business unit's charter, which is to help top hyperscalers and enterprise cloud providers harness Xilinx intelligent, adaptable infrastructure to improve performance, power efficiency and operating costs.

With Xilinx for 14 years, Raje most recently served as executive vice president, software and IP products, overseeing related strategy, design and execution. He has led initiatives in Vivado HLx and SDx development environments that have allowed customers to adopt higher-level abstraction flows, and greatly contributed to expansion efforts in machine learning and vision applications. Raje also spearheaded the introduction of the Vivado Design Suite, which brought ASIC-class algorithms and user interfaces to FPGA designers. As a result, Xilinx improved customer productivity and became the leader in design tools and IP.

Raje is a 13-year veteran of Xilinx. Prior to joining Xilinx, he was CTO, then CEO, of Hier Design Inc. which he co-founded in 2001. At Hier Design, he was responsible for introducing a hierarchical design and design planning tool, PlanAhead, to the FPGA design community. While CEO, he oversaw the acquisition of Hier Design by Xilinx Inc. in 2004.

Prior to Hier Design, Raje was a director at Monterey Design Systems, responsible for placement and silicon virtual prototyping technology. He started his career at IBM T. J. Watson Research Center in Yorktown Heights, New York working in high-level synthesis.

Raje holds a Bachelor of Technology in Electrical Engineering from IIT, Madras and a Master of Science and a Ph.D. in Computer Science from Northwestern University. He holds eight patents in the areas of electronic design tools, ASIC and FPGA designs, and has authored over 15 industry recognized research papers.



**Emre Önder**  
**Senior Vice President, Marketing**

Emre Önder leads marketing at Xilinx, including portfolio planning, product marketing, corporate marketing, and vertical industry marketing. Önder is accountable for accelerating growth in Xilinx's core vertical markets, comprised of approximately \$1.7 billion in revenue and 20,000 customers in aerospace and defense; automotive; test, measurement and emulation; audio, video and broadcast; industrial, scientific and medical; and consumer.

Since joining Xilinx, Önder has unified the marketing functions, and placed early emphasis on platforms to make Xilinx's adaptable solutions accessible to more customers in both traditional and emerging applications such as AI inference, ADAS, autonomous drive, and highly integrated RF and mMIMO applications. In addition, he spearheaded Xilinx's 2018 global developer conference – Xilinx Developer Forum (XDF).

Önder has over 25 years of general management experience as a leader of global organizations, including marketing, strategic alliances, channel and strategic account sales, engineering, and new product development. Önder has a track record of creating growth in mature businesses, as well as complex, emerging markets.

Prior to joining Xilinx, Önder served as vice president/general manager and vice president, Global Sales for Honeywell Sensing & IoT. Previously, he served as vice president of marketing and core markets at Analog Devices, where he grew the company's broad-market segment business from \$700 million to over \$1.2 billion annual revenue. He also led its global channel and award-winning B2B digital strategy. Earlier in his career, Önder worked at the Boston Consulting Group and EMC Corporation in strategic planning and vertical business development roles.

Önder earned a Bachelor of Science Degree in Electrical Engineering and a Master of Business Administration from Stanford University. He is a recipient of the F.E. Terman Engineering Award and is an Arjay Miller Scholar.



**Ramine Roane**  
**Vice President of Software and AI Product Marketing**

Ramine Roane is vice president of software and AI product marketing at Xilinx. At XDF 2019, he is spearheading the introduction of Vitis, Xilinx's new unified software platform for its ACAP and FPGA devices, and Vitis AI, both of which are essential to the company's goal of expanding in data center and intelligent edge applications.

Roane was instrumental in the introduction of the Vivado Design Suite in 2012, a modern electronic design automation (EDA) tool that helped give Xilinx a solid software foundation for hardware programmability.

Prior to joining Xilinx in 2010, Roane held roles in management and software architecture in large software companies, as well as FPGA start-ups.

He holds a Masters in Electronic Engineering and Computer Science from the National Polytechnic Institute of Grenoble (INPG), France.



**Wayne Lyons**  
**Director of Automotive and Strategic Customer Marketing**

As marketing director for Xilinx, Wayne works closely with several companies at the forefront of Advanced Driver Assistance Systems (ADAS) in Europe. As a result, he is closely involved in the identification and requirements of future platforms. These platforms include time-of-flight solutions such as LiDAR and RADAR designs along with vision platforms such as front and surround view camera systems. In addition to ADAS, Wayne works closely with designers of future safety domain platforms which aggregate multiple channels of sensors to provide advanced safety features and autonomous driving services. Wayne currently holds a position on the advisory board for the IS Auto Europe Conference.

Prior to Xilinx, Wayne spent twenty years at Arm, working in several roles including IP licensing and heading Asia Pacific sales for Arm, where he worked closely with several key companies in the embedded and automotive markets. His prior experience involves global marketing for Arm's embedded market and the introduction of the highly successful Cortex-M family of microcontroller cores. During this time he held a board advisory position with Luminary Micro prior to their successful acquisition by Texas Instruments.

Wayne began his career working in the semiconductor market for Hitachi (now Renesas). He holds a master's degree in Electronic and Electrical Engineering from Loughborough University in the UK and is a Member of the IET.