Design Services Partner Profile

AMD XILINX

MVD Cores Company Overview

Partner Details

Partner Tier:	Select
Headquarters Location:	9,impasse de la bastière,BLAN,,France
Xilinx Alliance Partner Since:	6/11/2001

Key Contact Info

Michael BESLON
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Regions Served

APAC, Central and South America, Eastern Europe and Russia, India, Japan, Middle East, North America Central, North America East, North America West, Western Europe

Supported Product Types

Yes	Design Services	No	Embedded Products
No	IP	No	EDA Tools
No	Boards		

Engineering Service Types

Yes	General Purpose Services	
Yes	Services in Support of Products Sold	Preferred
No	Services Leading to Contract Manufacturing	
Maximum Service Scope:	Chip level FPGA / ASIC design	

Engagement Models Supported

Yes	SOW-Based Fixed Bid Projects	Preferred
No	Time and Materials	
At my location	Captive Resources at Partner or Customer Premises	

Xilinx Design Tool Experience

Yes	Vivado	No	SDSoC
No	HLx	No	SDAccel

Industry Certifications Achieved

Industry Certification Services Offered

Government Clearances

None

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MVD Cores Segment and Application Focus

No Focus	**Defense/Aerospace**	No Focus	Homeland Security
		No Focus	Electronic Warfare
		No Focus	ISR
		No Focus	Space
		No Focus	Missiles & Munitions
		No Focus	Military Avionics
		No Focus	Commercial Avionics
		No Focus	SatComm
		No Focus	Wireless Milcom
		No Focus	Other A&D

Primary Focus	**Audio, Video, Broadcast**	No Focus	Encoders & Decoders
		No Focus	Monitors & Projection
		Primary Focus	Networking & Converters
		No Focus	Audio Systems
		No Focus	Video Processing Cards
		No Focus	Video Conferencing
		Primary Focus	Routers and Switches
		No Focus	Pro Cameras & Camcorders
		Primary Focus	Other Broadcast

No Focus	**Automotive & Transport**	No Focus	Driver Assist - Camera
		No Focus	Driver Assist Radar/LIDAR
		No Focus	Driver Info/Infotainment
		No Focus	Automated Driving
		No Focus	Other Automotive

No Focus	**Consumer**	No Focus	Digital Display
		No Focus	Drones
		No Focus	Multifunction Printers
		No Focus	Other Consumer

No Focus	**Datacenter**	No Focus	Storage
		No Focus	Compute
		No Focus	Networking

Secondary Focus	**Industrial, Medical**	No Focus	Motor Control
		Primary Focus	Industrial Networking
		No Focus	Surveillance
		No Focus	Machine Vision
		No Focus	Medical Imaging
		Secondary Focus	Medical / Clinical
		No Focus	Other ISM

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MVD Cores Segment and Application Focus (continued)

No Focus	**Test & Measurement**	No Focus	Simulation / Emulation
		No Focus	T & M Instrumentation
		No Focus	Semiconductor Test
		No Focus	Wired Communication Test
		No Focus	Wireless Communications Test
		No Focus	Other Test & Measurement

Secondary Focus	**Wired Comms & Networks**	No Focus	Access Equipment
		No Focus	Long Haul/Transport
		No Focus	Wired Mobile Backhaul
		No Focus	OTN
		No Focus	Enterprise Equipment
		No Focus	Ethernet Interlaken Connectivity
		Primary Focus	Packet Processing
		Secondary Focus	Other Wired Communications

Secondary Focus	**Wireless Comms & Networks**	No Focus	Baseband
		Secondary Focus	Wireless Backhaul
		No Focus	Connectivity & Switching
		No Focus	Radio
		Secondary Focus	Other Wireless

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Design Competencies

System Architecture	No Focus	
RF and Analog Design	No Focus	
FPGA Design and Integration	Primary Focus	IP cores intégration, ISE/VIVADO projects building, UCF, FPGA place and route, FPGA timing optimisation, Bitstream generation
FPGA Timing Closure	Secondary Focus	When IP cores integration is made by MVD Cores
FPGA Design Optimization	Secondary Focus	When IP cores integration is made by MVD Cores
Signal Integrity Design and Analysis	No Focus	
Inhouse or Subcontracted:		
Typical Serial Speed:	0	
PCB Design & Layout	No Focus	
Inhouse or Subcontracted:		
Industrial and Mechanical Design	No Focus	
Inhouse or Subcontracted:		
Industry Certification and Compliance Testing	No Focus	
Inhouse or Subcontracted:		
Quality and Reliability Testing	No Focus	
Inhouse or Subcontracted:		
Contract Manufacturing	No Focus	
Inhouse or Subcontracted:		
Max Quantities Supported:		
Digital Signal Processing	Secondary Focus	Signal processing in Digital TV modulators IP cores
Experience:	Up Sampling, Up Conversion, Filters	
Embedded Processors	Secondary Focus	Zynq, MicroBlaze, PicoBlaze, Mico32
Experience:		

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Embedded Porting /   No Focus

BSP Extension

    OS and RTOS Design Experience:

Embedded Application   No Focus

Development

Image Sensor Fusion

      Homo or Heterogeneous:

   Sensor Types Design Experience:

Computer Vision

Machine Learning

     Framework Experience:

    Network Model Experience: