Design Services Partner Profile

AMD XILINX

Comcores ApS Company Overview

Partner Details

Partner Tier:	Select
Headquarters Location:	Haldor Topsøes Allé 1,Kongens Lyngby,,Denmark
Xilinx Alliance Partner Since:	1/22/2015

Key Contact Info

Yogesh Dhancholia
ydh@comcores.com

Regions Served

Supported Product Types

Yes	Design Services	No	Embedded Products
Yes	IP	No	EDA Tools
No	Boards		

Engineering Service Types

Yes	General Purpose Services	
Yes	Services in Support of Products Sold	Preferred
Yes	Services Leading to Contract Manufacturing	
Maximum Service Scope:	Chip level FPGA / ASIC design	

Engagement Models Supported

Yes	SOW-Based Fixed Bid Projects	Preferred
Yes	Time and Materials	
At my location	Captive Resources at Partner or Customer Premises	

Xilinx Design Tool Experience

Yes	Vivado	No	SDSoC
No	HLx	No	SDAccel

Industry Certifications Achieved

Industry Certification Services Offered

Government Clearances

None

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Comcores ApS Segment and Application Focus

Primary Focus	**Defense/Aerospace**		
		No Focus	Homeland Security
		No Focus	Electronic Warfare
		No Focus	ISR
		No Focus	Space
		No Focus	Missiles & Munitions
		No Focus	Military Avionics
		No Focus	Commercial Avionics
		No Focus	SatComm
		No Focus	Wireless Milcom
		No Focus	Other A&D

No Focus	**Audio, Video, Broadcast**		
		No Focus	Encoders & Decoders
		No Focus	Monitors & Projection
		No Focus	Networking & Converters
		No Focus	Audio Systems
		No Focus	Video Processing Cards
		No Focus	Video Conferencing
		No Focus	Routers and Switches
		No Focus	Pro Cameras & Camcorders
		No Focus	Other Broadcast

Primary Focus	**Automotive & Transport**		
		No Focus	Driver Assist - Camera
		No Focus	Driver Assist Radar/LIDAR
		No Focus	Driver Info/Infotainment
		No Focus	Automated Driving
		No Focus	Other Automotive

No Focus	**Consumer**		
		No Focus	Digital Display
		No Focus	Drones
		No Focus	Multifunction Printers
		No Focus	Other Consumer

Secondary Focus	**Datacenter**		
		No Focus	Storage
		No Focus	Compute
		No Focus	Networking

Secondary Focus	**Industrial, Medical**		
		No Focus	Motor Control
		No Focus	Industrial Networking
		No Focus	Surveillance
		No Focus	Machine Vision
		No Focus	Medical Imaging
		No Focus	Medical / Clinical
		No Focus	Other ISM

Comcores ApS Segment and Application Focus (continued)

Secondary Focus	**Test & Measurement**	No Focus	Simulation / Emulation
		No Focus	T & M Instrumentation
		No Focus	Semiconductor Test
		No Focus	Wired Communication Test
		No Focus	Wireless Communications Test
		No Focus	Other Test & Measurement

Primary Focus	**Wired Comms & Networks**	No Focus	Access Equipment
		No Focus	Long Haul/Transport
		No Focus	Wired Mobile Backhaul
		No Focus	OTN
		No Focus	Enterprise Equipment
		No Focus	Ethernet Interlaken Connectivity
		No Focus	Packet Processing
		No Focus	Other Wired Communications

Primary Focus	**Wireless Comms & Networks**	No Focus	Baseband
		No Focus	Wireless Backhaul
		No Focus	Connectivity & Switching
		No Focus	Radio
		No Focus	Other Wireless

Design Services Partner Profile

Design Competencies

System Architecture	Primary Focus	
RF and Analog Design	No Focus	
FPGA Design and Integration	Primary Focus	Comcores develop IP-cores ready for integration. These are provided as ready-to-use components. Comcores offer integration services for these components as well. Within front-hauling and radio systems Comcores consider ourse
FPGA Timing Closure	Primary Focus	
FPGA Design Optimization	Primary Focus	
Signal Integrity Design and Analysis	No Focus	
Inhouse or Subcontracted:		
Typical Serial Speed:	0	
PCB Design & Layout	No Focus	
Inhouse or Subcontracted:		
Industrial and Mechanical Design	No Focus	
Inhouse or Subcontracted:		
Industry Certification and Compliance Testing	No Focus	
Inhouse or Subcontracted:		
Quality and Reliability Testing	No Focus	
Inhouse or Subcontracted:		
Contract Manufacturing	No Focus	
Inhouse or Subcontracted:		
Max Quantities Supported:		
Digital Signal Processing	Primary Focus	IP-components is offered for digital signal processing. Some key IP's are: I/Q Compression IP, digital up-conversion IP and digital down-conversion IP
Experience:	Own designs on Xilinx FPGA	
Embedded Processors	No Focus	
Experience:		
Embedded Porting / BSP Extension	No Focus	
OS and RTOS Design Experience:		
Embedded Application Development	No Focus	

Design Services Partner Profile

Image Sensor Fusion

Homo or Heterogeneous:

Sensor Types Design Experience:

Computer Vision

Machine Learning

Framework Experience:

Network Model Experience: